

USB OTG Transceiver

FZOTG111HC0H

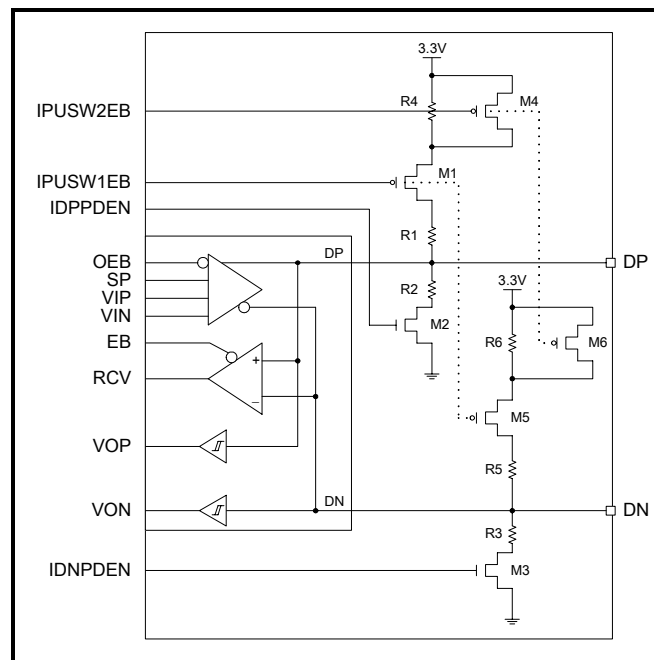
Key Features

- UMC 0.13μm 1.2V/3.3V 1P8M logic high speed FSG enhancement process
- Operating voltage range: 1.08V ~ 1.32V
- Operating junction temperature range: -40°C ~ 125°C
- IP's minimum metal requirement: 4 metal layers (4M)
- 3.3V power supply (I/O) with 5V tolerance
- Compliant with USB specification 1.1
- Compliant with USB On-The-Go supplement specification 1.0
- Built-in pull-up / pull-down resistors
- Support full speed data transfer rate, 12 Mbps
- Support low speed data transfer rate, 1.5 Mbps
- ESD protection > 3kV (HBM)
- IP's minimum oxide requirement: dual oxide (2G)

General Description

This FZOTG111HC0H, a USB On-The-Go transceiver cell, is designed using the UMC 0.13μm L130E / HS logic process. It uses a 3.3V power supply to interface with the USB On-The-Go physical layer specification. It is capable of providing both full speed (12 Mbps) and low speed (1.5 Mbps) data transfer rates.

Block Diagram



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