

## 0.15μm Standard Performance FSB0G\_A PCIX 133MHz I/O Cells

### Key Features

- UMC's 0.15μm 1.5V/3.3V Logic Standard Performance Process
- Support PCI33, PCI66, PCIX 133MHz
- 3.3V input, 3.3V output drive
- Input buffer with programmable pull up resistance, pull down resistance,
- Built-in Antenna diodes for all pins
- ESD Robustness and Latch-up immunity proven by Silicon

### General Description

This library is tailored for UMC's 0.15μm 1.5V/3.3V Logic Standard Performance Process. All I/Os are equipped with a rich selection of programmable features capable of adapting to a wide variety of application environments. These I/O cells are designed based on "PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a" to meet PCI-X application. The PCI-X specification is specified to establish a high performance local bus standard for several generations of products.

### Quick Reference

Characteristic		Description
<b>Physical</b>	Process	UMC 0.15μm 1.5V/3.3V 1P7M SP Logic Process
	Cell Width * Cell Height	43.2μm * 238.56μm (staggered) 62.88μm * 142.08μm (In-line)
	Cell Layout Metal	M1, M2, M3
	Power Ring Metal	M3 and above
	Layout Resolution	0.005μm
<b>Electrical</b>	Recommended Operating Conditions	Power Supply Voltage Core: 1.35V ~ 1.65V I/O: 3.0V ~ 3.6V
		Junction Temperature: -40°C ~ 125°C
	Driving Strength	Meet PCIX/PCI66MHz/PCI33MHz driving specification
	Input Threshold	Follow PCI Local Bus Specification
	Pull Up/Down Resistance	75K Ohm

#### HEADQUARTERS

Hsinchu, Taiwan,  
 Tel: 886-3-578-7888  
 Fax: 886-3-578-7889  
 sales@faraday-tech.com

#### USA OFFICE

Sunnyvale, USA  
 Tel: 1-408-522-8888  
 Fax: 1-408-522-8889  
 sales@faraday-usa.com

#### EUROPE OFFICE

Hoofddorp, Netherlands  
 Tel: 31-2356-20496  
 Fax: 31-2356-36297  
 eusales@faraday-tech.com

#### JAPAN OFFICE

Tokyo, Japan  
 Tel: 81-3-5214-0070  
 Fax: 81-3-5214-0076  
 sales@faraday.co.jp

#### CHINA OFFICE

Shanghai, P. R. China  
 Tel: 86-21-6406-7523  
 Fax: 86-21-6406-5327  
 sales@faraday.com.cn