

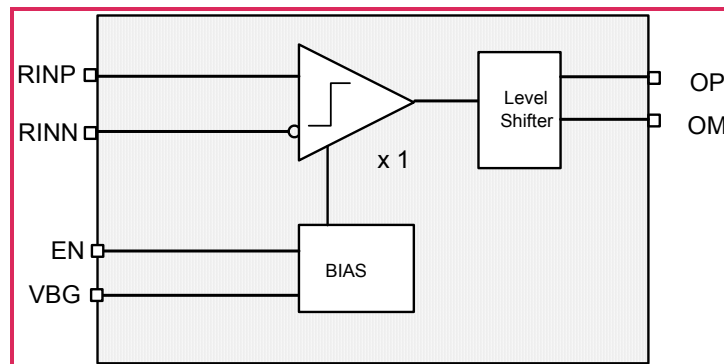
Key Features

- UMC 0.18 μm logic GII process
- Operating voltage range: 2.97 V ~ 3.63 V
- Operating junction temperature range: 0 °C ~ 115 °C
- Minimum metal requirement: 3 metal layers
- Supports the data rates up to 630 Mbps
- Compatible with IEEE1596.3 and TIA/EIA-644 SCI LVDS standards
- Power-down mode supply current < 1 mA

General Description

The FXLV RX020HA0A is a 0.18 μm CMOS for the differential signal receiver designed for the applications requiring ultra-low power dissipation and high data transfer rate. It supports the data rates up to 630 Mbps utilizing the Low Voltage Differential Signaling (LVDS) technology. The FXLV RX020HA0A accepts the low voltage differential input signals and translates them into 1.8 V/3.3 V CMOS TTL output levels. The cell works at an operating voltage range from 2.97 V to 3.63 V, and an operating junction temperature range between 0 °C and 115 °C.

Block Diagram





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