

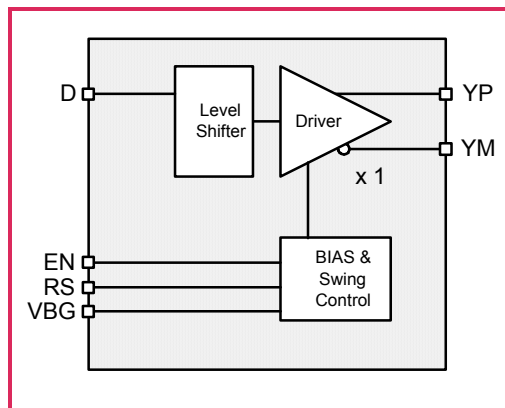
Key Features

- UMC 0.18 μm logic GII process
- Operating voltage range: 2.97 V ~ 3.63 V
- Operating junction temperature range: 0 $^{\circ}\text{C}$ ~ 115 $^{\circ}\text{C}$
- Minimum metal requirement: 3 metal layers
- Supports the data rates up to 630 Mbps
- Compatible with IEEE1596.3 and TIA/EIA-644 SCI LVDS standards
- Power-down mode supply current < 1 mA
- Voltage-mode transmitter with the potentials in the common mode as well as the swings in the differential mode .

General Description

The FXLVTX020HA0A is a 0.18 μm driver cell of the differential line transmitter processed with the CMOS and designed for the applications requiring the ultra-low power dissipation and high data transfer rate. It is designed to support the data rates up to 630 Mbps by utilizing the Low Voltage Differential Signaling (LVDS) technology. As shown in the block diagram, the FXLVTX020HA0A accepts the 1.8 V CMOS input signals and translates them to the low voltage differential output signals. This IP works at the operating voltage ranging from 2.97 V to 3.63 V, and with the junction temperature range between 0 $^{\circ}\text{C}$ and 115 $^{\circ}\text{C}$. The transmitter is based on the design characteristics of the voltage mode, and the potentials in the common mode as well as the swing in the differential mode, which can not be determined through the terminal resistor.

Block Diagram





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